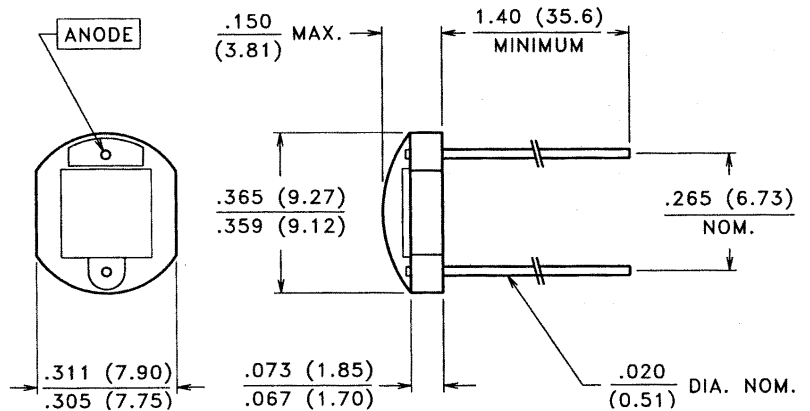


## PACKAGE DIMENSIONS inch (mm)



CASE 13 CERAMIC  
CHIP ACTIVE AREA: .032 in<sup>2</sup> (21 mm<sup>2</sup>)

## PRODUCT DESCRIPTION

Large area planar silicon mounted on a two lead ceramic substrate and coated with a layer of clear epoxy. Low junction capacitance permits fast response time.

## ABSOLUTE MAXIMUM RATINGS

Storage Temperature: -20°C to 75°C  
Operating Temperature: -20°C to 75°C

**RoHS Compliant**



## ELECTRO-OPTICAL CHARACTERISTICS @ 25°C (See also VTP curves, pages 45-46)

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	VTP4085H			VTP4085SH			UNITS
			Min.	Typ.	Max.	Min.	Typ.	Max.	
I <sub>SC</sub>	Short Circuit Current	H = 100 fc, 2850		200			200		μA
TC I <sub>SC</sub>	I <sub>SC</sub> Temperature Coefficient	2850 K		.20			.20		%/°C
I <sub>SC</sub>	Short Circuit Current	100 μW/cm <sup>2</sup> , 940 nm	11.4	15		11.4	15		μA
V <sub>OC</sub>	Open Circuit Voltage	H = 100 fc, 2850 K		.33			.33		mV
TC V <sub>OC</sub>	V <sub>OC</sub> Temperature Coefficient	2850 K		-2.0			-2.0		mV/°C
I <sub>D</sub>	Dark Current	H = 0, VR = 100 V			100		15	50	nA
R <sub>SH</sub>	Shunt Resistance	H = 0, V = 10 mV		2.0			4.0		MΩ
TC R <sub>SH</sub>	R <sub>SH</sub> Temperature Coefficient	H = 0, V = 10 mV		-11			-11		%/°C
C <sub>J</sub>	Junction Capacitance	H = 0, V = 0 V		.35			.35		nF
λ <sub>range</sub>	Spectral Application Range		400		1100	400		1100	nm
λ <sub>p</sub>	Spectral Response - Peak			925			925		nm
S <sub>R</sub>	Sensitivity	@ Peak		.55			.55		A/W