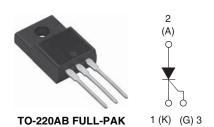


Vishay High Power Products

Phase Control SCR TO-220AB FULL-PAK, 25 A



PRODUCT SUMMARY		
V _T at 16 A	< 1.25 V	
I _{TSM}	200 A	
V _{RRM}	800/1200 V	

DESCRIPTION/FEATURES

The 25TTS...FPPbF High Voltage Series of silicon controlled rectifiers are specifically designed for medium power switching and phase control applications. The glass passivation technology used has reliable operation up to 140 °C junction temperature.

Typical applications are in input rectification (soft start) and these products are designed to be used with Vishay HPP input diodes, switches and output rectifiers which are available in identical package outlines. Fully isolated package ($V_{INS} = 2500 \ V_{RMS}$); plastic material $94V_{Ro}$.

This product has been designed and qualified for industrial level and lead (Pb)-free ("PbF" suffix).

OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS		
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C, common heatsink of 1 °C/W	18	22	А		

MAJOR RATINGS AND CHARACTERISTICS					
PARAMETER	TEST CONDITIONS	VALUES	UNITS		
I _{T(AV)}	Sinusoidal waveform	16	۸		
I _{RMS}		25	A		
V _{RRM} /V _{DRM}		800/1200	V		
I _{TSM}		300	А		
V _T	16 A, T _J = 25 °C	1.25	V		
dV/dt		500	V/µs		
dl/dt		150	A/µs		
TJ		- 40 to 125	°C		

VOLTAGE RATINGS						
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{DRM} , MAXIMUM PEAK DIRECT VOLTAGE V	I _{RRM} /I _{DRM} AT 125 °C mA			
25TTS08FPPbF	800	10				
25TTS12FPPbF	1200	1200	10			

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
PARAWETER		TEST CONDITIONS	TYP. MAX.	UNITS
Maximum average on-state current	I _{T(AV)}	T _C = 85 °C, 180° conduction half sine wave	16	
Maximum RMS on-state current	I _{RMS}		25	_
Maximum peak, one-cycle,	-	10 ms sine pulse, rated V _{RRM} applied	300	Α
non-repetitive surge current	I _{TSM}	10 ms sine pulse, no voltage reapplied	350	
Maximum 12+ for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	450	A ² s
Maximum I ² t for fusing	1-1	10 ms sine pulse, no voltage reapplied	630	A-S
Maximum I $^2\sqrt{t}$ for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied	6300	A²√s
Maximum on-state voltage drop	V_{TM}	16 A, T _J = 25 °C	1.25	V
On-state slope resistance	r _t	T 105 °C	12.0	mΩ
Threshold voltage	V _{T(TO)}	T _J = 125 °C	1.0	V
Maximum various and divest leakage accurant	1 /1	T _J = 25 °C	0.5	
Maximum reverse and direct leakage current	I _{RM} /I _{DM}	T _J = 125 °C V _R = Rated V _{RRM} /V _{DRM}	10	^
Holding current	I _H	Anode supply = 6 V, resistive load, initial $I_T = 1 A$	- 100	mA
Maximum latching current	ΙL	Anode supply = 6 V, resistive load	200	
Maximum rate of rise of off-state voltage	dV/dt		500	V/µs
Maximum rate of rise of turned-on current	dl/dt		150	A/µs

TRIGGERING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum peak gate power	P_{GM}		8.0	w
Maximum average gate power	$P_{G(AV)}$		2.0	
Maximum peak positive gate current	+ I _{GM}		1.5	Α
Maximum peak negative gate voltage	- V _{GM}		10	V
Maximum required DC gate current to trigger	I _{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	60	mA
		Anode supply = 6 V, resistive load, T _J = 25 °C	45	
		Anode supply = 6 V, resistive load, T _J = 125 °C	20	
Maximum required DC gate voltage to trigger	V_{GT}	Anode supply = 6 V, resistive load, T _J = - 10 °C	2.5	
		Anode supply = 6 V, resistive load, T _J = 25 °C	2.0	V
		Anode supply = 6 V, resistive load, T _J = 125 °C	1.0	
Maximum DC gate voltage not to trigger	V_{GD}	T = 105 °C V = Betad value	0.25	
Maximum DC gate current not to trigger	I_{GD}	T _J = 125 °C, V _{DRM} = Rated value		mA

SWITCHING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Typical turn-on time	t _{gt}	T _J = 25 °C	0.9	
Typical reverse recovery time	t _{rr}	T ₁ = 125 °C	4	μs
Typical turn-off time	t _q	1j=125 C	110]

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THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 125	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.5	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	1.5	
Approximate weight				2	g
Approximate weight				0.07	oz.
Mounting torque	minimum	6 (5)	kgf · cm		
Mounting torque	maximum			12 (10)	(lbf · in)
			0	25TTS08FP	
Marking device			Case style TO-220AB FULL-PAK (94/V0)	25TTS1	2FP

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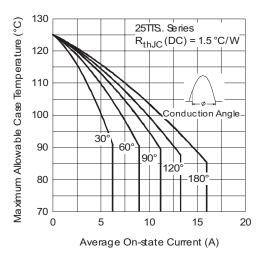


Fig. 1 - Current Rating Characteristics

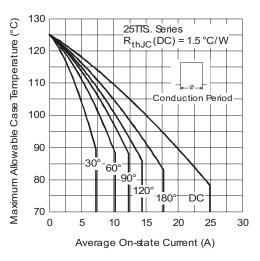


Fig. 2 - Current Rating Characteristics

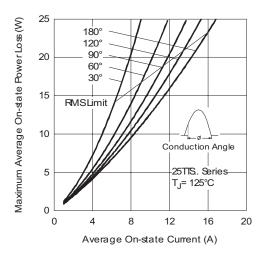


Fig. 3 - On-State Power Loss Characteristics

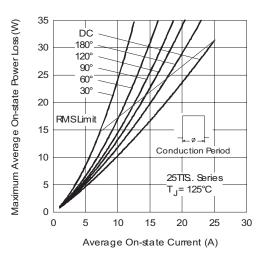


Fig. 4 - On-State Power Loss Characteristics

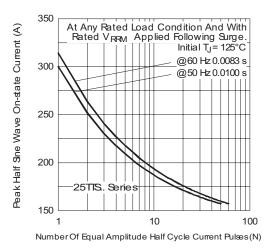


Fig. 5 - Maximum Non-Repetitive Surge Current

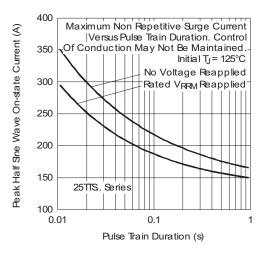


Fig. 6 - Maximum Non-Repetitive Surge Current



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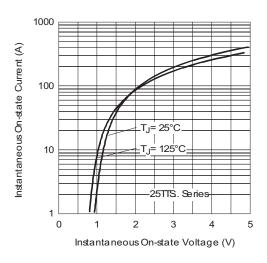


Fig. 7 - On-State Voltage Drop Characteristics

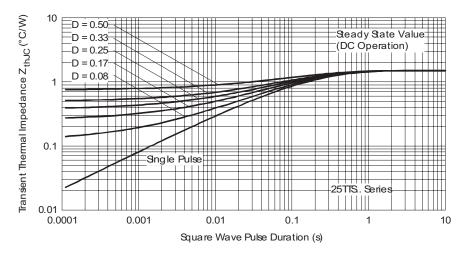


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

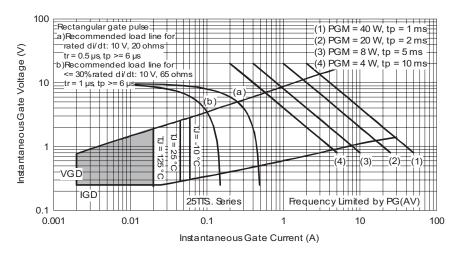


Fig. 9 - Gate Characteristics

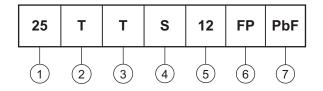
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (25 = 25 A)
- 2 Circuit configuration:

T = Single thyristor

3 - Package:

T = TO-220AB

4 - Type of silicon:

Standard recovery rectifier

FULL-PAK

- FOLL-FAR

None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95072				
Part marking information http://www.vishay.com/doc?95069				

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